

FIG. 1 Prior Art

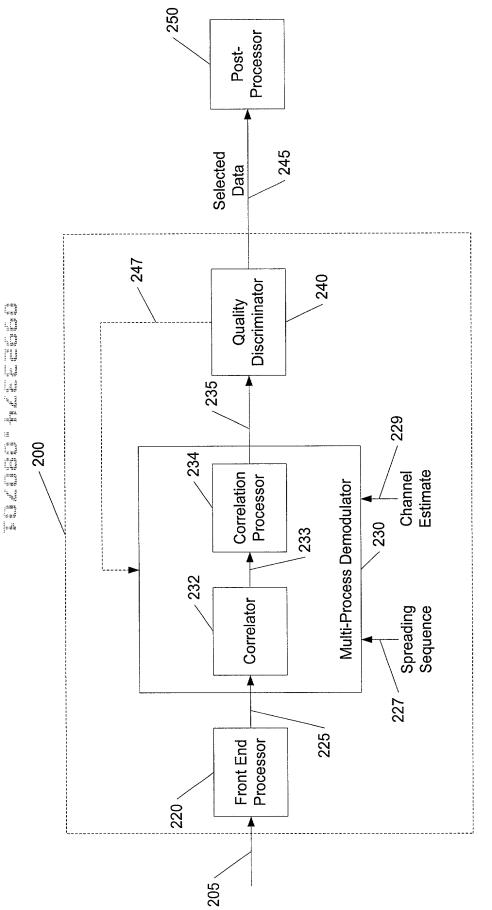


FIG. 2

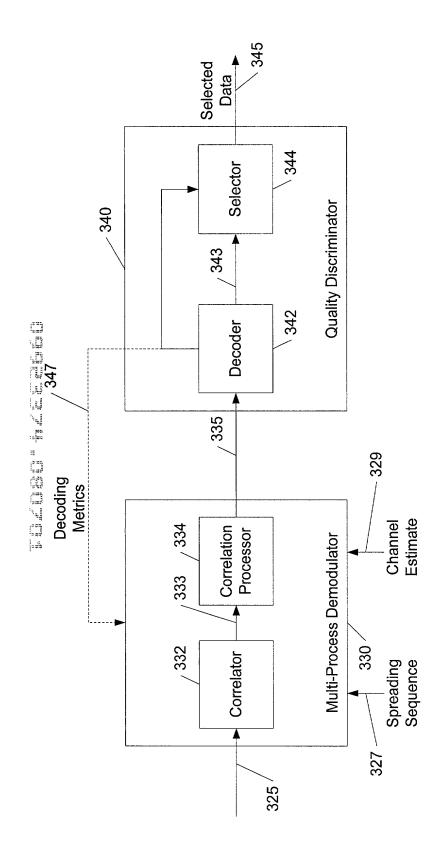


FIG. 3

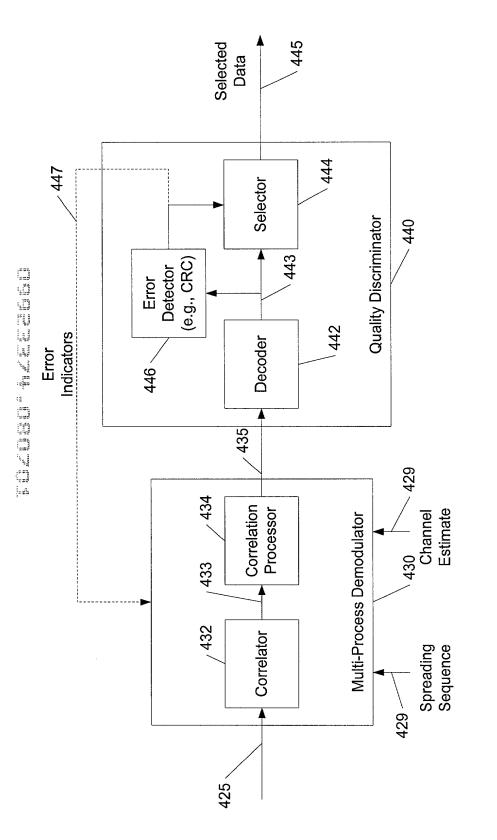
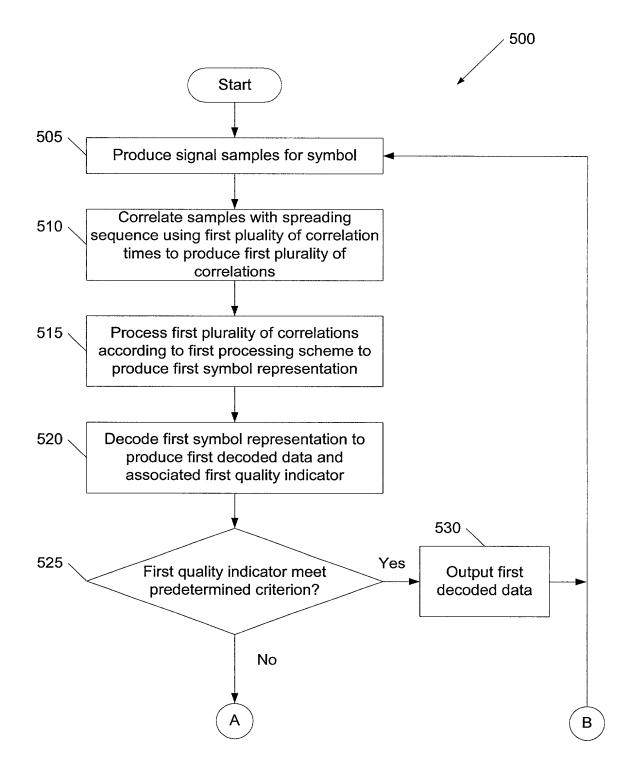


FIG. 4



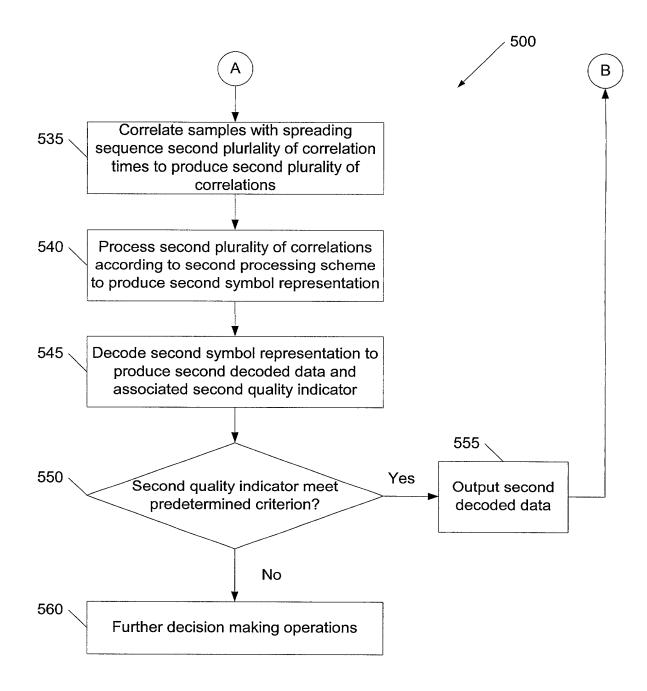
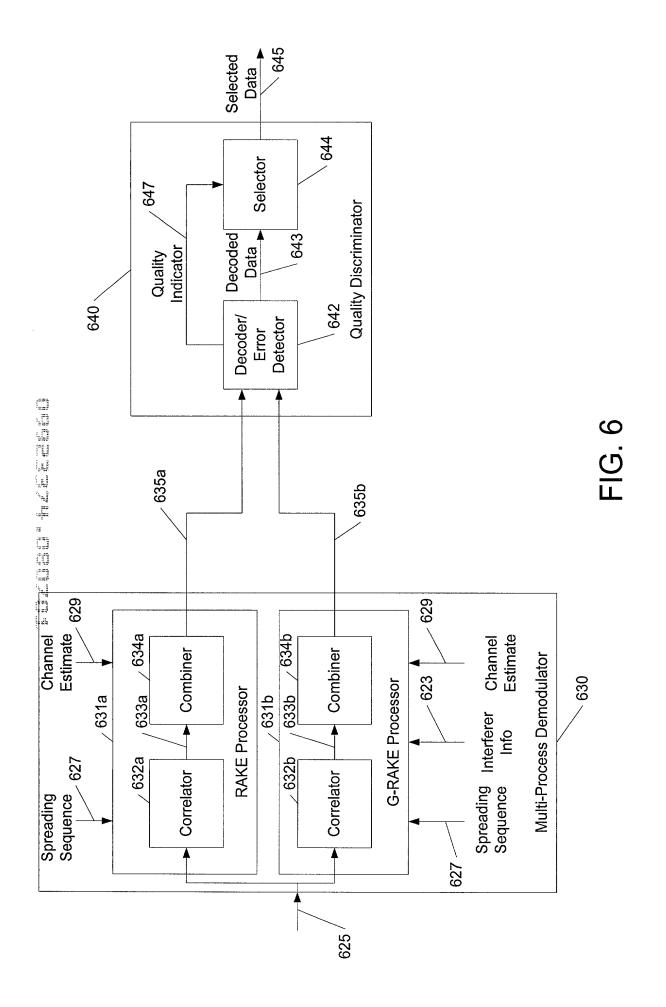


FIG. 5B



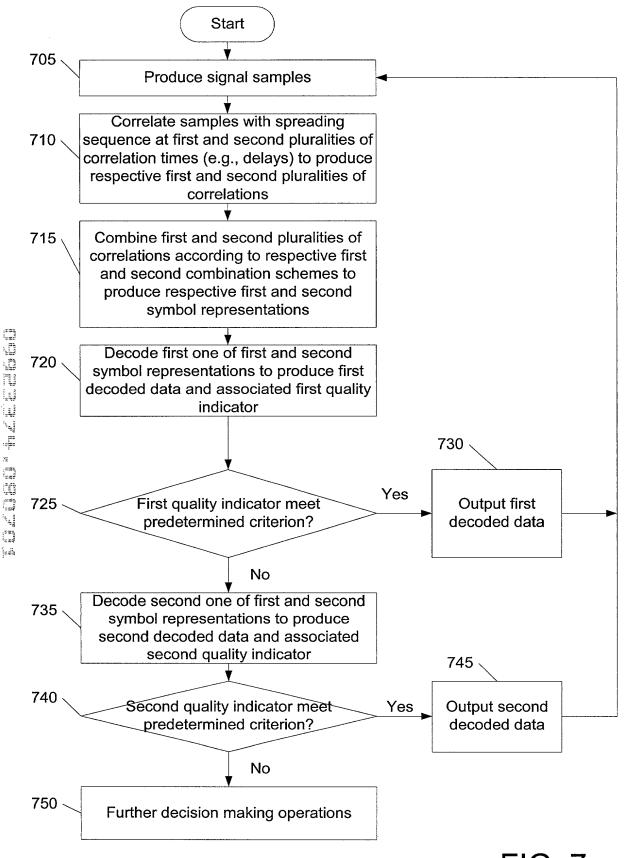
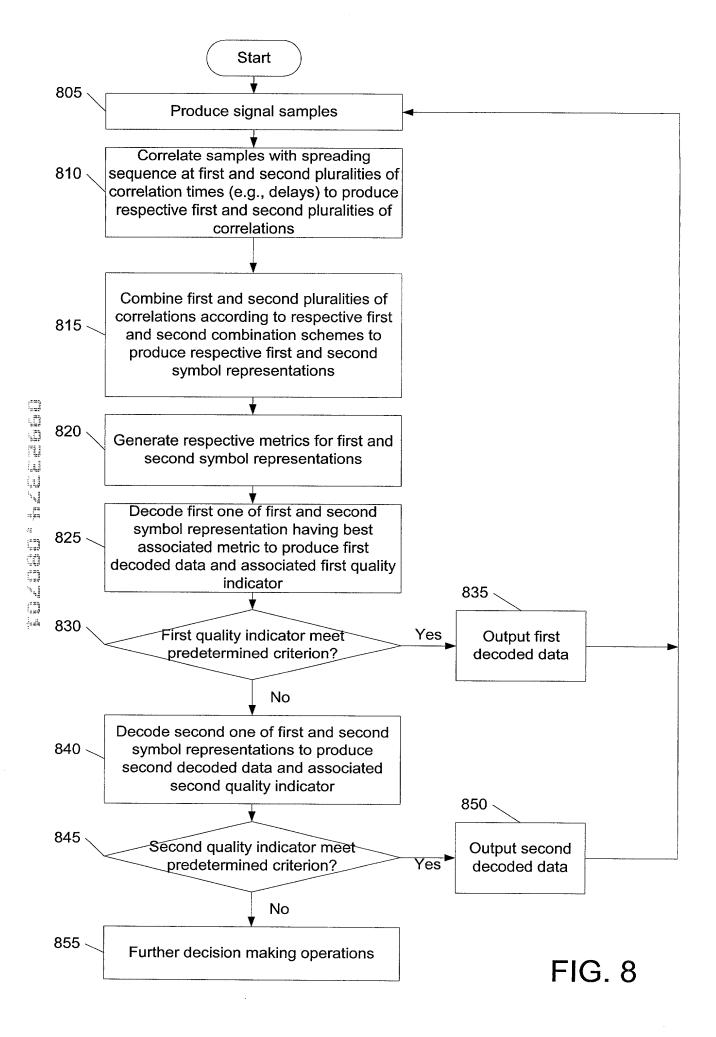
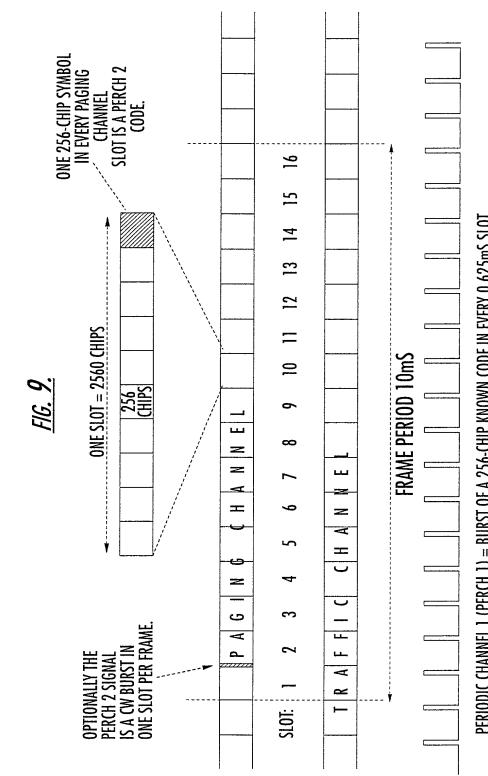


FIG. 7





PERIODIC CHANNEL 1 (PERCH 1) = BURST OF A 256-CHIP KNOWN CODE IN EVERY 0.625mS SLOT

